

Amendments to the Specification:

Please replace the paragraph bridging pages 14 and 15 with the following amended paragraph:

Referring to FIG. 7, a plurality of external terminals 2, including input/output terminals (TMin/TMout) 23, a power supply terminal (Vdd) 21 and a ground terminal (GND) 22, are arranged at the peripheral regions of a semiconductor substrate 1. An internal circuit 4, which is an integrated circuit functioning as a memory cell array, is formed inside the semiconductor substrate 1. The internal circuit 4 is connected to the external ~~circuits~~terminals 2 by way of interface circuits 3. A power supply line 24 and a ground line 25, which are connected to the power supply terminal 21 and the ground terminal 22, respectively, are formed on the semiconductor substrate 1.